

Mark McDermott, P. E.

Mark McDermott has 29 years of experience in the management and product development of silicon systems. Mark is also a serial entrepreneur, having co-founded six companies during his career. He has led engineering teams in the development of PowerPC processors and Intel x86 processors and has 19 patents in the area of microprocessor design and test.

Mark is currently the VP Engineering at Coherent Logix, Inc. In addition he is Principal and General Partner in the Silicon Web Group, a consulting firm focused on EDA startups. Prior to this he was the CEO of DynaFlow Computing, Inc., VP Engineering and co-founder of Somerset Embedded Technologies, Inc., VP Engineering and co-founder of VisionFlow, Inc., General Manager and Director of the Texas Development Center for Intel Corporation, Director of the PowerPC Somerset Design Center and Director of the Austin Design Center for Cyrix, Inc. Mark co-founded Accelerated Solutions Corp. in 1984 and MonoCom Systems, Inc. in 1981. Previous hardware and software design experience includes: low-power microprocessors, speech synthesizers, cache controllers, logic simulation hardware accelerators, micro-coded bit-slice processors, traffic controllers, industrial controllers and communication controllers.

His current interests include research on how to improve design and verification productivity in today's silicon system designs. He is an Adjunct Assistant Professor in ECE Department at the University of Texas where he teaches graduate level courses in VLSI design, System-on-Chip design and technical entrepreneurship. He is on the technical advisory boards for three engineering design automation tool companies: Obsidian Software Inc., Nascentric, Inc. and Pyxis, Inc.

Mark received a Bachelors degree in Electrical Engineering from the University of New Mexico and a Masters in Electrical Engineering from the University of Texas. Mark is a registered professional engineer and a member of the IEEE, ACM and NSPE/TSPE. He has a number of publications in the areas of IC design and engineering management.